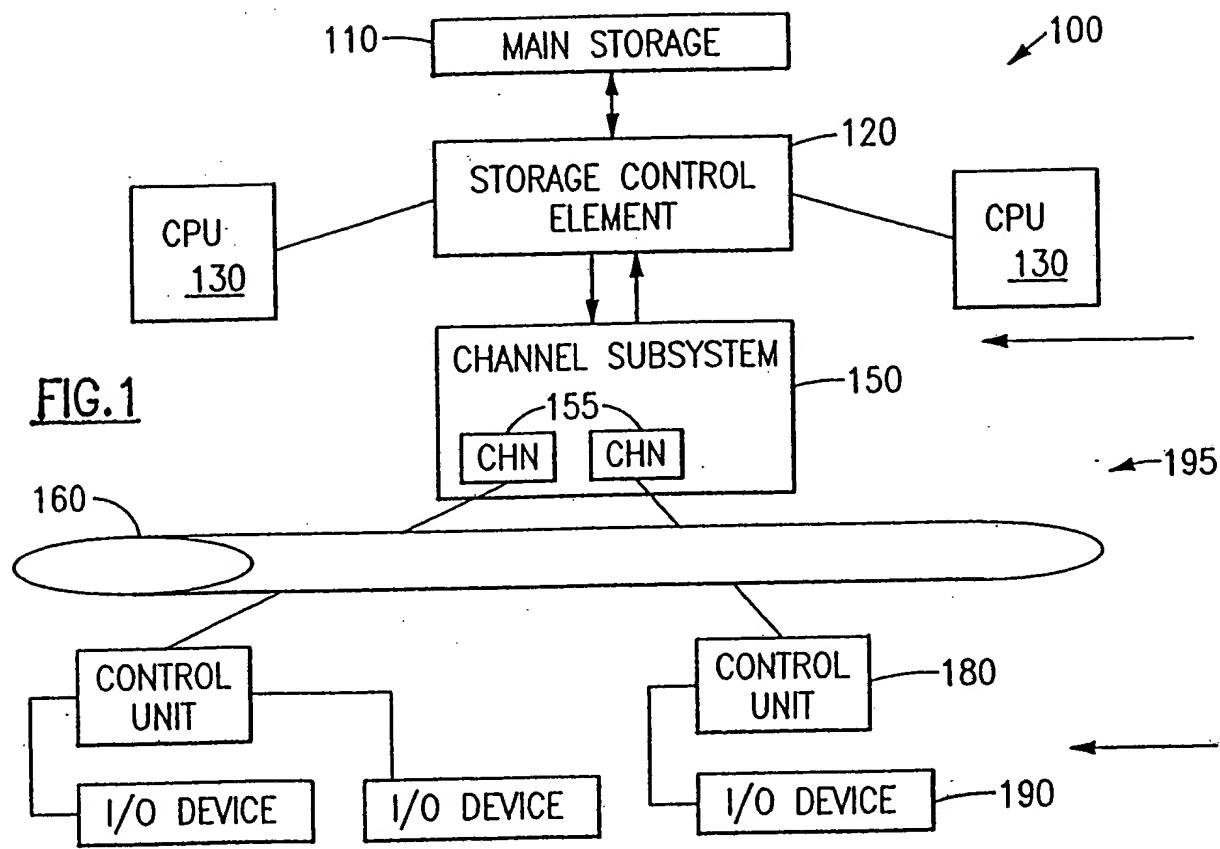


P00920000173 US1
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215

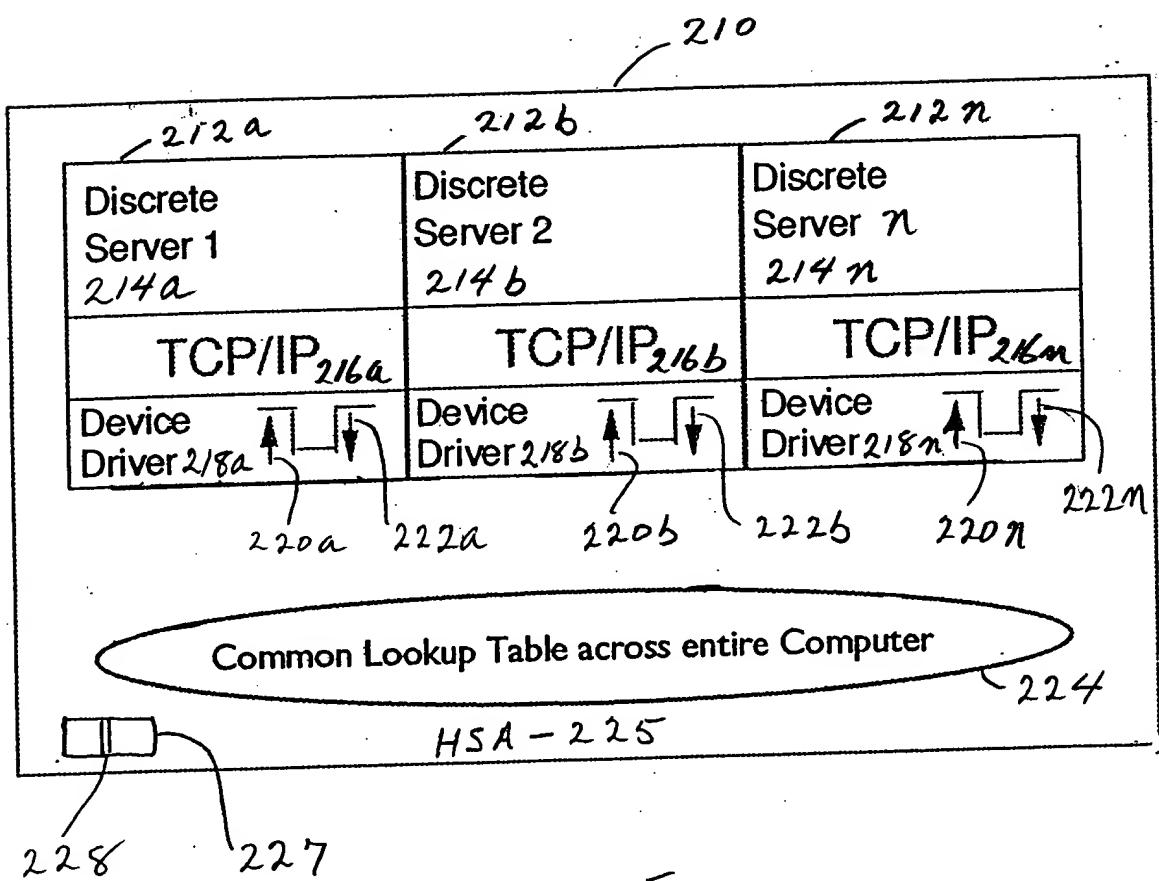
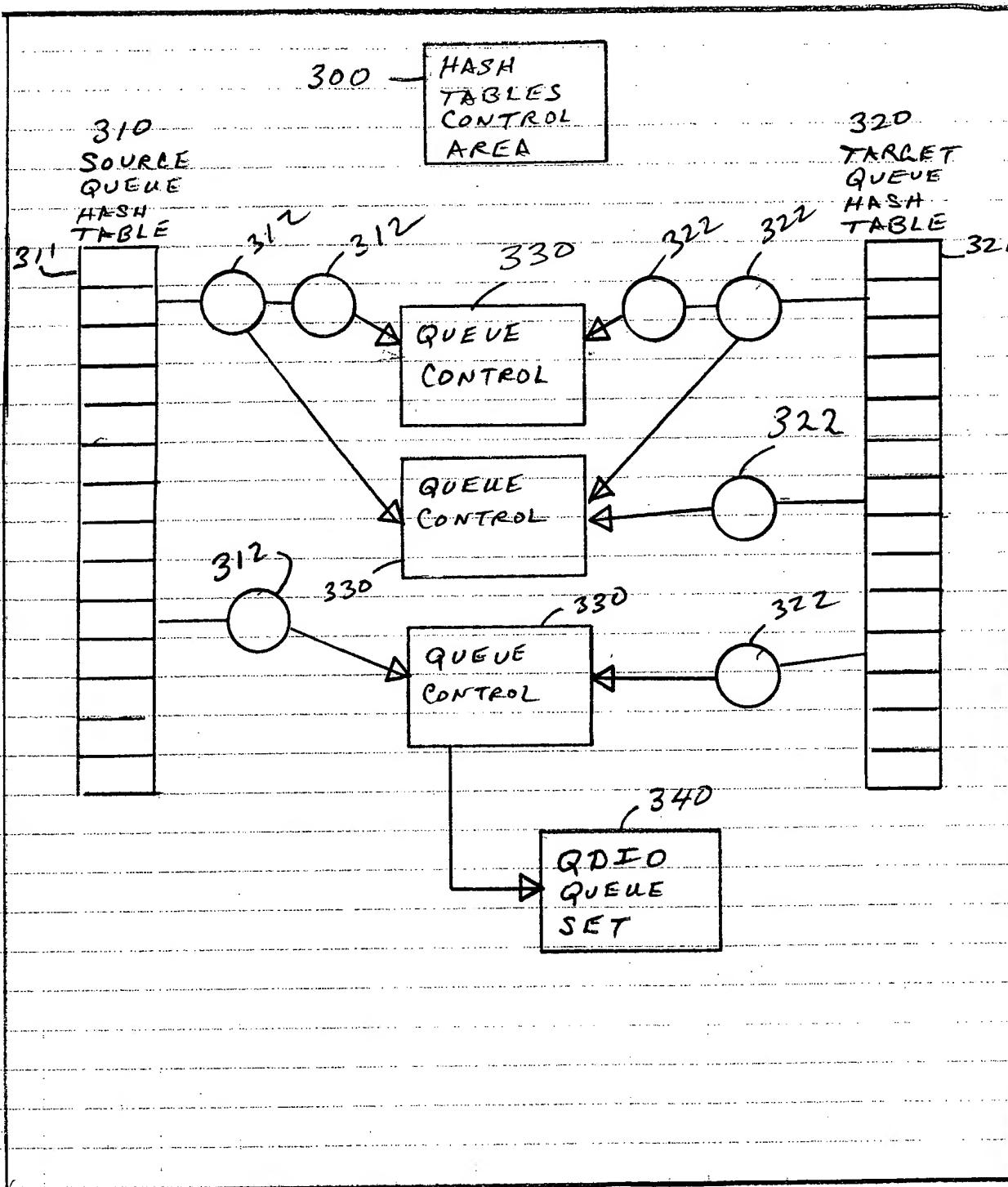


FIG 2

POU9200001730SI
315



224

FIG. 3

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415

300 - HASH TABLE CONTROL

401 -	HASH TABLE SHARED SERIAL LOCK
402 -	HASH TABLE EXCLUSIVE UPDATE LOCK

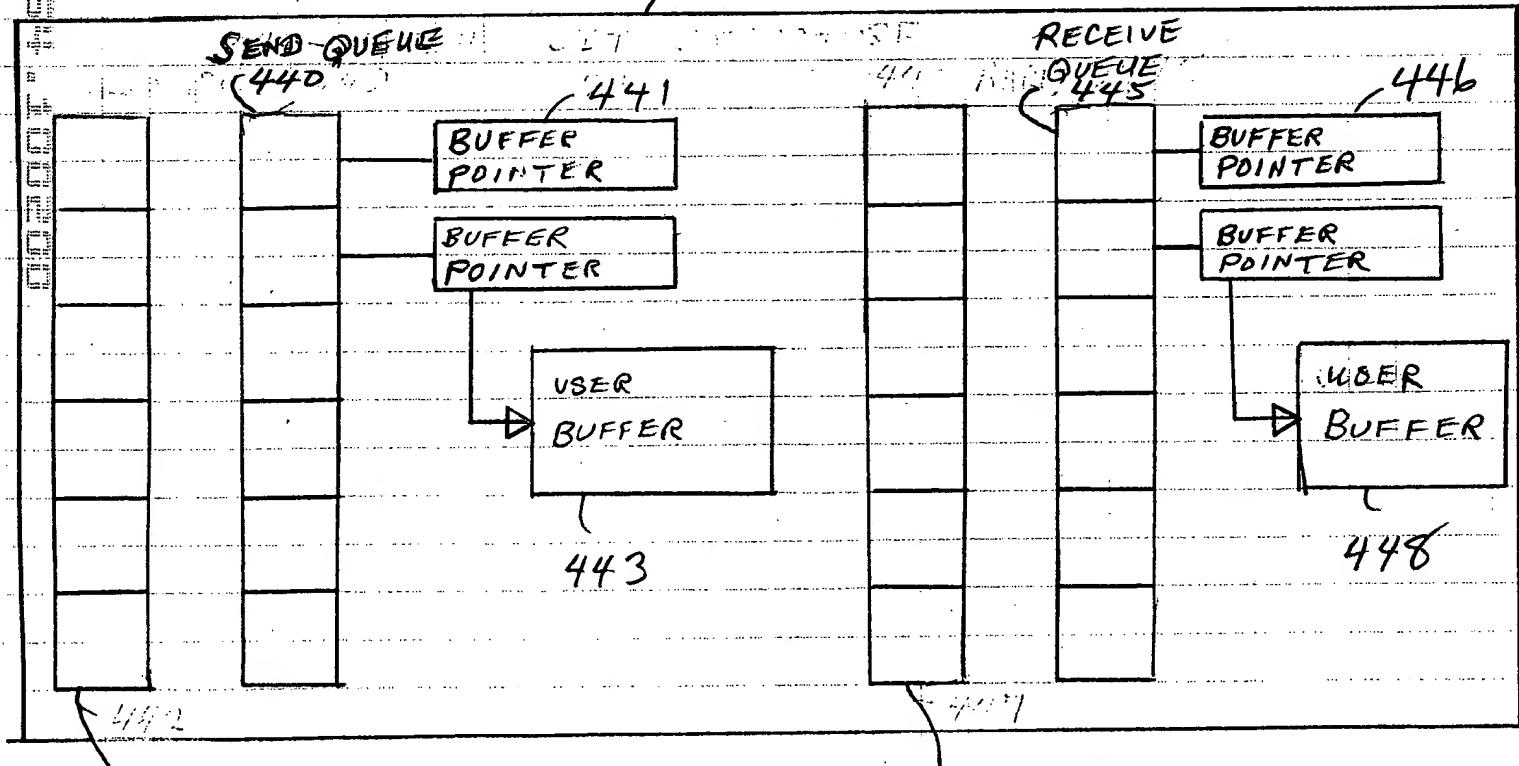
FIG. 4

330 - QUEUE CONTROL

430 -	QDIO POINTER TO SET 340
431 -	OUTBOUND LOCK
432 -	INBOUND LOCK

FIG. 5

QUEUE SET 340



STORAGE
LIST
STATUS
BLOCK
442

FIG. 6

STORAGE
LIST
STATUS
BLOCK
447

243 USER BUFFER

244 TARGET IP ADDRESS

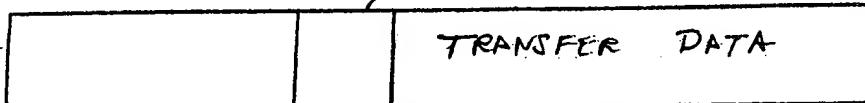


FIG. 7

312 SOURCE HASH DUPLICATE LIST ENTRIES

410 - LPAR-IP-SUBCHANNEL#

411 - STATUS

412 - POINTER TO QUEUE CONTROL 330

413 - NEXT POINTER TO 312

414 - PREVIOUS POINTER TO 311 OR 312

FIG. 8

322 TARGET HASH DUPLICATE LIST ENTRIES

420 - IP ADDRESS

421 - STATUS

422 - POINTER TO QUEUE CONTROL 330

423 - NEXT POINTER TO 322

424 - PREVIOUS POINTER TO 321 OR 322

FIG. 9